

## Performance and Analysis of Z-Source Isolated Bidirectional DC-DC Converter by Using PWM Control Strategy

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### Abstract

In This paper we are presenting an Bidirectional DC-DC converter is also known as dual active bridge (DAB) dc-dc converter. We presented a Z-source with bidirectional dc-dc converter by reducing the switching count by adding an passive elements we are improving the output voltage level, Comparing with the traditional bidirectional dc-dc converter, the proposed converter has an wider regulation range of voltage. The fully bridge symmetrical circuit configuration, is neither a high-voltage side nor a low-voltage side in the circuit structure, and the sources connected to the dc side of each H-bridge circuit with voltage sources and current sources. This method can reduce current stress and improves the system efficiency. Both simulation results shown by using an MATLAB software.

**Index Terms**— Bidirectional dc-dc converter (BDC), coordinated control, double-closed loop, , Z-source.

### I. INTRODUCTION

**BIDIRECTIONAL** dc-dc converter (BDC) is a two quadrant operating dc-dc converter. It is also known as dual active bridge converter. With the increasing need for electric power in future automobiles there is an increasing requirement for bi-directional

DC/DC converters to transfer energy between different voltage levels i.e high voltage side and low voltage, such as between the low voltage accessories and the high voltage drive train. The hybrid system input sources like solar, fuel cells (FCs) and super capacitor (SCs) as an environmentally renewable energy system has been applied in many fields, such as hybrid electric vehicle, uninterruptible power supply (UPS).

Thus, a secondary power source is required to compensate the power difference between the fuel cell and the load, and a battery is generally used to supply a transient power. The power flow between the fuel cell and the battery is managed by a bidirectional dc/dc converter. Conventional isolated bidirectional dc/dc converters for high-power applications have a voltage-fed full-bridge (FB) (VF-FB) method in the high-voltage (HV) side and various current-fed (CF) schemes in the low-voltage (LV) side in general because voltage-fed half-bridge (HB) and voltage-fed push-pull (PP) schemes have disadvantages of high current stress and/or high voltage stress. A two-stage isolated/bidirectional dc/dc converter adopting a current ripple reduction technique is proposed. The resonant converter with two bridges takes in charge of electrical isolation and constant gain, and the bidirectional control is accomplished using only the second stage with a

single bridge. To reduce rms currents in the HV source and link capacitor, capacitor division and synchronizing operation of two stages are adopted.

The isolated BDC (IBDC) is needed to provide absolute electrical isolation between the primary side and the secondary side for protecting the equipment and operators IBDC is usually based on the single phase and H-bridge topology with an isolation transformer. Fig. 1 shows a typical configuration of Bidirectional DC-DC converter. From the aspect of circuit structure, it has the following points are: 1) The high-frequency transformer provides the required isolation and voltage matching between two voltage levels; 2) transformer voltage ratio is not one, and both sides of the circuit are defined as the high-voltage side compared to the low-voltage side, which makes the installation location of power sources for both sides not interchangeable; 3) the sources connected to the dc side of each H-bridge can only be voltage sources; and 4) the transformer's leakage inductance serves as the instantaneous energy storage device.

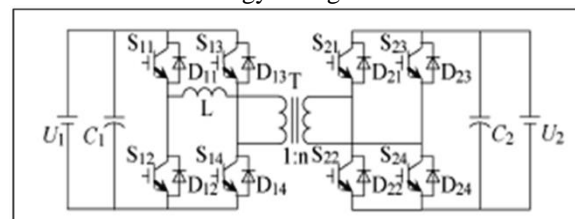


Fig.1 Schematic of the DAB dc-dc converter

This paper presented a Bidirectional DC-DC converter is also known as dual active bridge (DAB) dc-dc converter. We presented a Z-source with bidirectional dc-dc converter by reducing the switching count by adding an passive elements we

are going to increase the output voltage level. Comparing with the traditional bidirectional dc-dc converter, the proposed converter has a wider regulation range of voltage and many application based converter for hybrid vehicles and for any hybrid application. The fully bridge symmetrical circuit configuration, is neither a high-voltage side nor a low-voltage side in the circuit structure, and the sources connected to the dc side of each H-bridge circuit with voltage sources and current sources. This method can reduce current stress and improve the system efficiency.

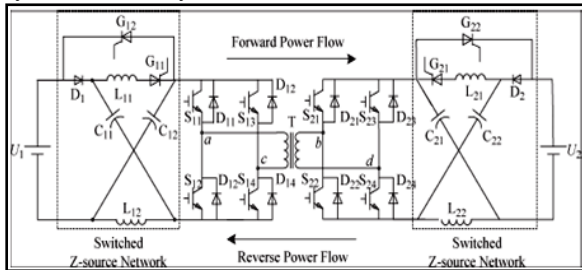


Fig.2 Schematic circuit of an Z- sources with bidirectional dc-dc converter

## II. MODEL OF DAB CONVERTER

### A. Basic Principle of Operation

In Fig. 1 consists of two full-bridge circuits connected through an isolation transformer and a coupling inductor  $L$ , which may be provided partly or entirely by the transformer leakage inductance. The full bridge on the left hand side of Fig. 1 is connected to the HV dc bus and the full bridge on the right hand side is connected to the low-voltage (LV) side. Each bridge is controlled to generate an HF square-wave voltage at its terminals. By incorporating an appropriate value of coupling inductance, the two square-waves can be suitably phase shifted with respect to each other to control power flow from one dc source to another. Thus, bidirectional power flow is enabled through a small lightweight HF transformer and inductor combination, and power flows from the bridge generating the leading square-wave. Although various modes of operation of the DAB converter have been presented for high power applications the square-wave mode is supposedly the best Operating mode.

This is because imposing quasi-square-wave on the transformer primary and secondary voltages results in trapezoidal, triangular, and sinusoidal waveforms of inductor current in the bidirectional DC-DC converter. These modes are beneficial for extending the low-power operating range of the converter. Although these modes tend to reduce the switching losses, the voltage loss is significant due to zero voltage which reduces the effective power transfer at high-power levels. The key operating waveforms of the converter during buck mode, i.e., when power flows from the HV side to the LV side are shown. The voltages generated by the two full

bridges, VHV on the HV side and VLV on the LV side, are represented as square-wave voltages with 50% duty cycle. But proposed circuit is with Z sources network i.e Switched Z-sources bidirectional DC-DC converter

### B. Boost Model

According to the analysis, the upper and lower devices of each phase leg in both H-bridges can be gated on simultaneously, so its reliability is improved greatly. Furthermore, the shoot-through zero state brings the boost ability to the Z sources We also take the forward power flow as an example to analyze. Assuming that the inductors  $L11$  and  $L12$  and capacitors  $C11$  and  $C12$  have the same inductance and capacitance, respectively, the Z-source network becomes symmetrical. From the symmetry and the equivalent circuits, we have (3)

$$U_{C11} = U_{C12} = U_C \quad (1)$$

$$u_{L11} = u_{L12} = u_L \quad (2)$$

we have

$$u_L = U_1 - U_C \quad (3)$$

$$u_d = U_1 \quad (4)$$

$$u_i = U_C - u_L = 2U_C - U_1 \quad (5)$$

Where,  $u_d$  is the dc-link voltage of the switched Z-source network and  $u_i$  is the dc-link voltage of the H-bridge

we have

$$u_L = U_C$$

$$u_d = 2U_C \quad (6)$$

$$u_i = 0$$

Given that a switching cycle  $T$  of SZIBDC consists of two sections,  $T_0$  for the shoot-through zero state and  $T_1$  for the normal switch state and the open zero state, respectively, the average voltage of the inductor over one switching cycle should be zero in steady state; thus, from (3) to (8), we have

$$U_L = \overline{u_L} = [T_0 \cdot U_C + T_1 \cdot (U_1 - U_C)] / T = 0 \quad (7)$$

Where,  $T = T_0 + T_1$ , in which case (7) can then be expressed as

$$\frac{U_C}{U_1} = \frac{T_1}{T_1 - T_0} \quad (8)$$

In the normal switch state and the open zero state, by substituting (8) into (5),  $u_i$  can be expressed as

$$u_i = \frac{T}{T_1 - T_0} U_1 = \frac{\pi}{\pi - 2\alpha} U_1 \quad (9)$$

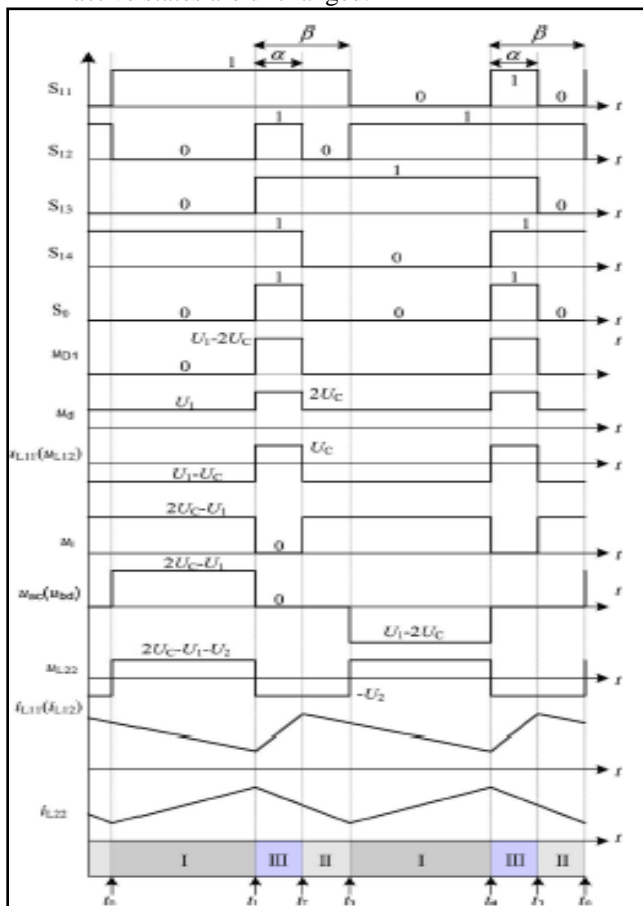
Therefore, we have

$$u_i = \begin{cases} 0, \\ \pi / (\pi - 2\alpha) U_1, \end{cases} \quad (10)$$

**C. Voltage Regulation Model**

In the traditional Z-source inverter, the regulation of voltage is achieved by the modulation index  $M$  and boost factor  $B$  which is unfit for the PWM in the SZIBDC.

Considering this, this paper presents a phase-shifting shoot through control method. Combining with the analysis of Section II, the main waveforms of SZIBDC in phase-shifting shoot-through control are shown in Fig. 6, where the transformer voltage ratio is one,  $S_0$  is the shoot-through pulse,  $u_{D1}$  is the voltage of the diode  $D1$ ,  $u_{ac}$  and  $u_{bd}$  are the voltages of the primary and secondary sides of the transformer, respectively, and  $i_{L11}$ ,  $i_{L12}$ , and  $i_{L22}$  are the currents flowing through the inductors  $L11$ ,  $L12$ , and  $L22$ , respectively. In the traditional phase-shifting control of the H-bridge, the converter is in the open zero state for an interval ( $t_0-t_2$ ,  $t_3-t_5$ ). By regulating the width of the interval during a switching cycle, the average output voltage can be stepped down. Additionally, from (12), if the shoot-through pulse ( $t_1-t_2$ ,  $t_4-t_5$ ) in the open zero state is added, the average output voltage can be stepped up. Note that the shoot-through zero states are evenly allocated into each phase and it does not change the total zero-state time interval. That is, the active states are unchanged.



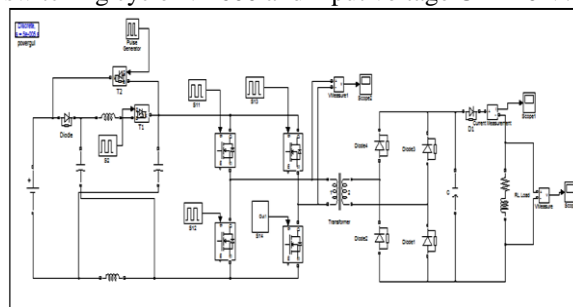
**Fig.3 Main waveforms of SZIBDC in phase-shifting shoot-through control**

**III. SIMULATION CIRCUIT AND RESULTS**

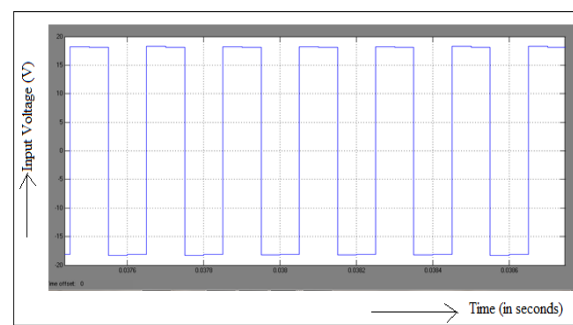
This paper presented a Z-sources with Bidirectional DC-DC converter. We presented a Z-source with bidirectional dc-dc converter by reducing the switching count by adding an passive elements to improve the system efficiency, we are going to increases level of output voltage, Comparing with the traditional bidirectional dc-dc converter, the proposed converter has an wider regulation range of voltage and many application based converter for hybrid vehicles and for any hybrid application. The fully bridge symmetrical circuit configuration, is neither a high-voltage side nor an low-voltage side in the circuit structure, and the sources connected to the dc side of each H-bridge circuit with voltage sources and current sources. This method can reduce current stress and improves the system efficiency.

**A. Simulation circuit of an forward mode  
 Main Parameters of SZIBDC**

In order to verify the correctness and validity of SZIBDC and its phase-shifting shoot-through bivariate coordinated control strategy, a simulation platform is established in electromagnetic transient simulation software Electro Magnetic Transients Including DC/Power System Computer Aided Design 4.2, and a laboratory prototype shown in Fig. 12 is constructed based on TMS320F2812 DSP. Furthermore, the main parameters of SZIBDC are as follows: inductors  $L11 = L12 = L21 = L22 = 0.1$  mH, capacitors  $C11 = C12 = C21 = C22 = 220$   $\mu$ F, transformer voltage ratio  $n = 1$ , switching frequency  $f = 5$  kHz, discretized values of a switching cycle  $N = 600$  and input voltage  $U1 = 20$  V.



**Fig 6.1 simulation arrangement for the proposed system in forward direction**



**Fig 6.2 input side voltage of transformer in forward mode**

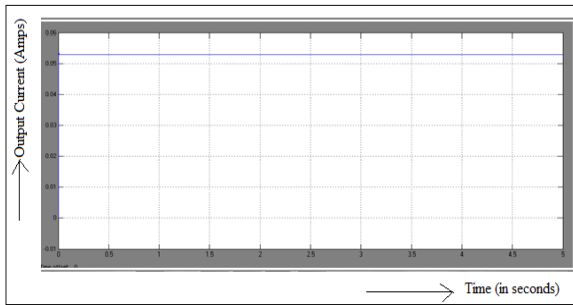


Fig 6.3 Output side current in forward direction mode

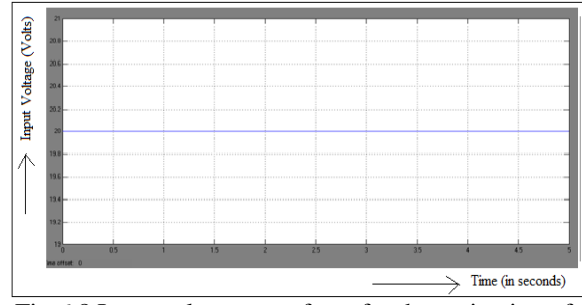


Fig 6.8 Input voltage waveform for the extinction of the system in forward direction mode

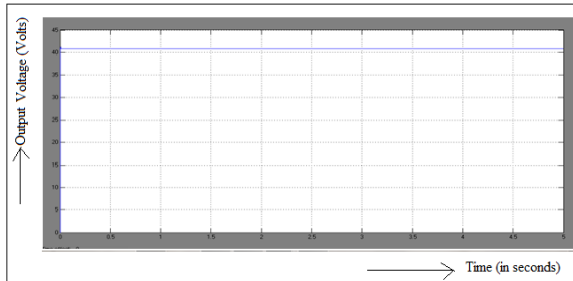


Fig 6.4 Output side voltage in forward direction mode

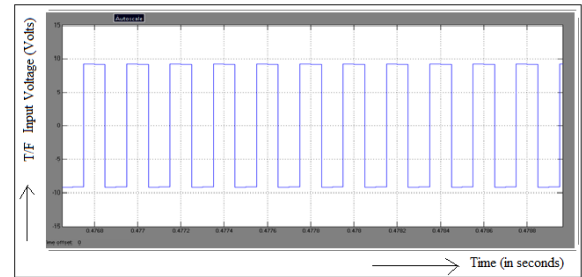


Fig 6.9 Transformer input voltage for the extinction of the system in forward direction mode

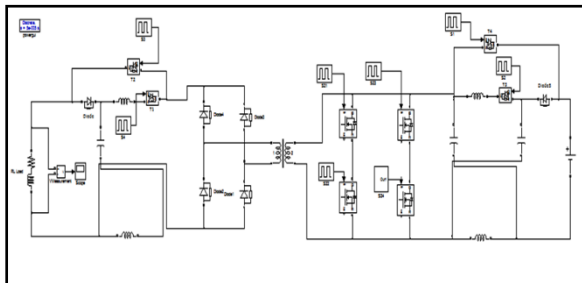


Fig 6.5 simulation arrangement for the proposed system in reverse direction

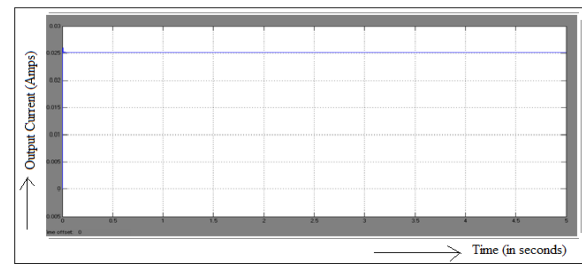


Fig 6.10 Output current waveform for the extinction of the work in forward direction mode

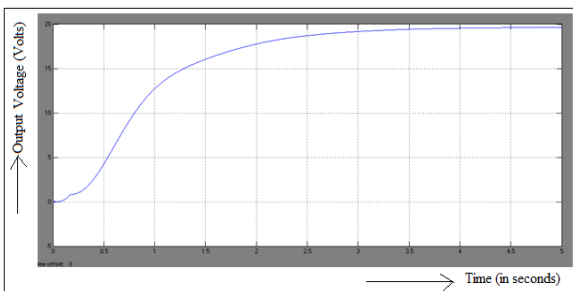


Fig 6.6 Output side voltage in reverse direction mode

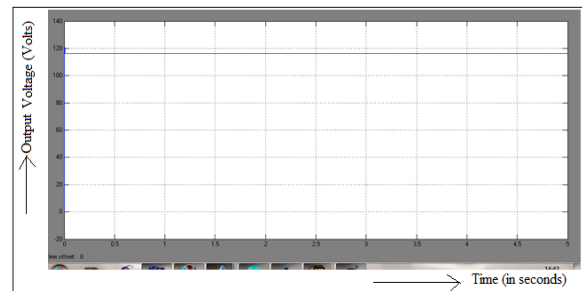


Fig 6.11 Output voltage waveform for the extinction of the system in forward direction mode

**B. Extinction of the work**

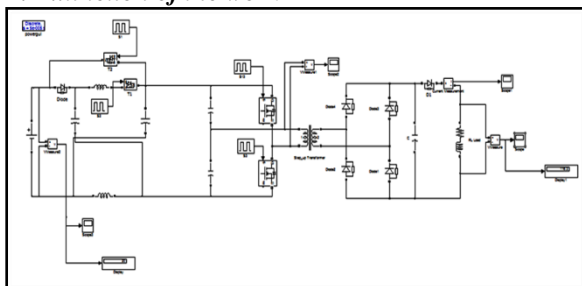


Fig 6.7 simulation arrangement for the proposed extinction of the system in forward direction

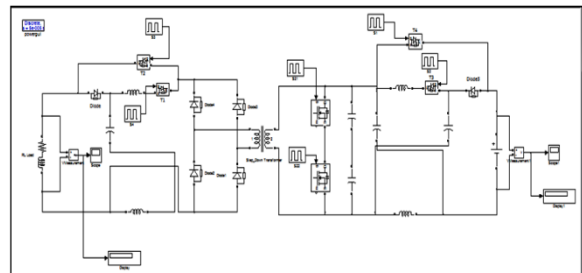


Fig 6.12 simulation arrangement for the proposed extinction of the system in reverse direction mode

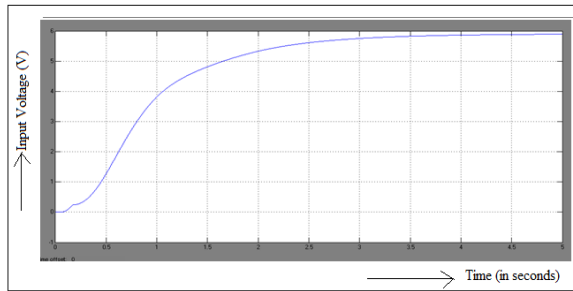


Fig 6.13 Input voltage for the extinction of the system in reverse direction mode

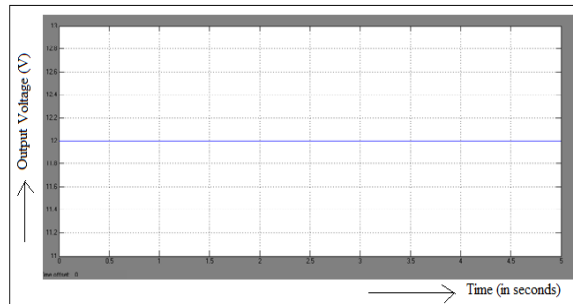


Fig 6.14 Output voltage waveform for the extinction of the system in reverse direction mode

### C. Simulation Analysis

In simulation, SZIBDC is in open-loop control for an interval  $t = 0-0.5$  s and in closed-loop control for an interval  $t = 0.5-2$  s. The output voltage reference is given the values of 10, 40, and 10 V for three intervals  $t = 0.5-1$  s,  $t = 1-1.5$  s, and  $t = 1.5-2$  s, respectively. Fig. 13 shows the simulation results for SZIBDC under different operating states. As can be seen from Fig. 13, in open-loop control ( $t = 0-0.5$  s),  $\alpha = \beta = 0$ , and the output voltage is 19 V which approximately equals the input voltage. When  $t = 0.5$  s, SZIBDC enters into bucking start-up closed-loop control ( $t = 0.5-1$  s), and the output voltage reference is 10 V. Due to  $\alpha = \beta = 0$  at this point, the control system works in control mode II,  $\alpha$  remains zero and  $\beta$  increases, the output voltage decreases rapidly and stabilizes in 10 V after 0.04 s, and  $\beta$  stabilizes in 1.56 rad.

When  $t = 1$  s, the output voltage reference jumps to 40 V, and SZIBDC jumps into boosting closed-loop control ( $t = 1-1.5$  s). Due to  $\beta > \alpha = 0$  at this point, the control system changes into control mode III,  $\alpha$  remains zero and  $\beta$  decreases, and the output voltage increases. When  $t = 1.003$  s,  $\beta$  decreased to be equal with  $\alpha$  while the output voltage is lower than 40 V, the control system changes into control mode IV,  $\alpha$  and  $\beta$  increase at the same speed, the output voltage increases rapidly and stabilizes in 40 V after 0.07 s, and  $\alpha$  and  $\beta$  stabilize in 1.06 rad.

When  $t = 1.5$  s, the output voltage reference jumps to 10 V again, and SZIBDC jumps into bucking closed-loop control ( $t = 1.5-2$  s). Due to  $\beta = \alpha > 0$  at this point, the control system changes into control mode I,  $\alpha$  decreases and  $\beta$  remains constant, and the output voltage decreases. When  $t = 1.535$  s,  $\alpha$

decreases to zero while the output voltage is higher than 10 V, the control system changes into control mode II,  $\beta$  increases, the output voltage decreases rapidly and stabilizes in 10 V after 0.03 s, and  $\beta$  stabilizes in 1.56 rad. Note that the final state of the process is in line with the previous bucking start-up closed-loop control.

### IV. CONCLUSION

We presented a Z-source with bidirectional dc-dc converter by reducing the switching count by adding an passive elements we are improving the output voltage level, Comparing with the traditional bidirectional dc-dc converter, the proposed converter has an wider regulation range of voltage. This method can reduce current stress and improves the system efficiency. Both simulation results shown by using an MATLAB software.

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